

Supersedes:
59301A-1

HP MODEL 59301A ASCII/PARALLEL CONVERTER
All Serial Numbers

HP-IB VERIFICATION PROGRAM USING THE 9825A

The following program checks the 59301A for proper operation of the HP-IB I/O only. To provide a complete functional check of the 59301A, this test must be used in combination with the Operating and Service manual in-cabinet performance test.

1. Figure A is the basic flow chart for the verification program
2. Verification using the 9825A Calculator
 - a. Set up the 9825A Calculator with the 98213A General I/O-Extended I/O ROM on the 98214A Plotter-General I/O-Extended I/O ROM, 98034A HP-IB Calculator interface (set to address 7), the 59301A ASCII/Parallel Converter and a 5150A Thermal Printer equipped with two Option 002's (BCD).
 - b. Set the 5150A controls as follows:

BCD OPTION 002 in the A2 position:

DATA ± TRUE	IN
CMD	OUT

BCD OPTION 002 in the A4 position:

DATA ± TRUE	IN
CMD	IN

IF CLOCK OPTION 004 is present:

PRINT/TIME	DISABLED.
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 - c. Connect a BCD cable (P/N 562A-16A/B/C) from J1 (59301A) to the A2 board. Connect a second BCD cable from J2 (59301A) to the A4 board.
 - d. Set the 59301A controls as follows:

CONTROL SWITCHES	0110001
------------------	---------

(Address switches)
 - e. Turn on the 9825A and 5150A. Initialize the 59301A by removing AC power (if already on), then reapplying power.
 - f. Insert the Data Cartridge (P/N 59300-10001) into the 9825A. Load and run file Ø (ldpØ).

AEO/erl/W

02-5/77

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- c. The V.P. sends the interface clear command (cli) and halts to verify:
CHECK POINT 3: LISTENING light OFF
- d. The V.P. sends data 1234567890 to address 10010 and halts to verify:
CHECK POINT 4: LISTENING light OFF
5150A output none
- e. END OF TEST. To repeat this test press RUN.

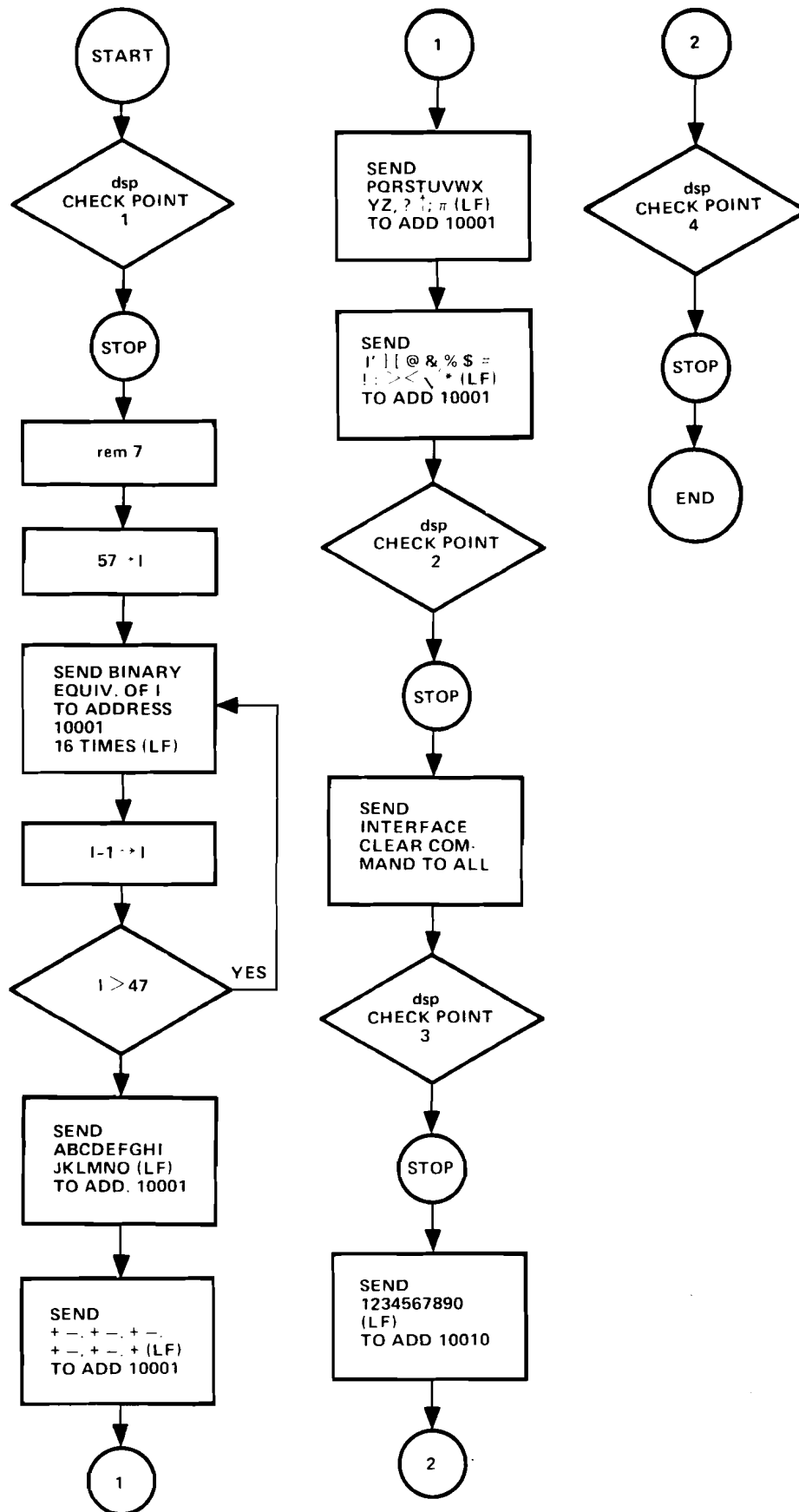


Figure A. 59301A ASCII/Parallel Converter Verification Program Flow Chart

```

0: prt "59301A","ASCII to","Parallel","Converter","Verification","Test"
1: spc 2
2: prt "-----","CHECK POINT 1"
3: prt "Verify the ","initial setup:"
4: prt "*ON          on","*LISTENING off";spc 2
5: dsp "CHECK POINT 1"
6: stp
7: prt "-----","CHECK POINT 2"
8: prt "Address the","59301A and send:","16 ones, 16 twos"
9: prt "16 threes, etc.,""Verify:"
10: prt "*LISTENING on"
11: prt "*5150A output:","0000000000000000","0000000000000000"
12: prt "+- +- +- +- +- +","0000000000000000","0000000000000000"
13: prt "1111111111111111","2222222222222222","          .","          ."
14: prt "9999999999999999"
15: spc 2
16: rem 7
17: 57→I
18: 1→J
19: wtb 717,I
20: 1+J→J
21: if J<17;gto 19
22: wrt 717
23: 1-1→I
24: if I>47;gto 18
25: wrt 717,"ABCDEFGHJKLMNO"
26: wrt 717,"+-.+-.+-.+-.+-.+"
27: wrt 717,"PQRSTUVWXYZ,?^;π"
28: wrt 717,"|'|[@&%$#!:><√*"
29: dsp "CHECK POINT 2"
30: stp
31: prt "-----","CHECK POINT 3"
32: prt "Send the IFC","command. Verify:","*LISTENING off";spc 2
33: cli 7
34: dsp "CHECK POINT 3"
35: stp
36: prt "-----"
37: prt "CHECK POINT 4","Send data"
38: prt "1234567890","to address 10010"
39: prt "Halt to verify:","*LISTENING off","*5150A output:"," None"
40: wrt 718,"1234567890"
41: dsp "CHECK POINT 4"
42: stp ;spc 2
43: prt "END OF TEST";spc 4
44: rew
45: end
*17070

```

Figure B. 59301A ASCII/Parallel Converter Verification Program Listing

```

59301A
ASCII to
Parallel
Converter
Verification
Test
-----
CHECK POINT 4
Send data
1234567890
to address 10010
Halt to verify:
*LISTENING off
*5150A output:
None

```

```

-----
CHECK POINT 1
Verify the
initial setup:
*ON on
*LISTENING off

```

```

-----
CHECK POINT 2
Address the
59301A and send:
16 ones, 16 twos
16 threes, etc.
Verify:
*LISTENING on
*5150A output:
00000000000000000000
00000000000000000000
+- +- +- +- +- +
00000000000000000000
00000000000000000000
11111111111111111111
22222222222222222222
.
.
99999999999999999999

```

```

-----
CHECK POINT 3
Send the IFC
command. Verify:
*LISTENING off

```

Figure C. 59301A Verification Program Sample Output