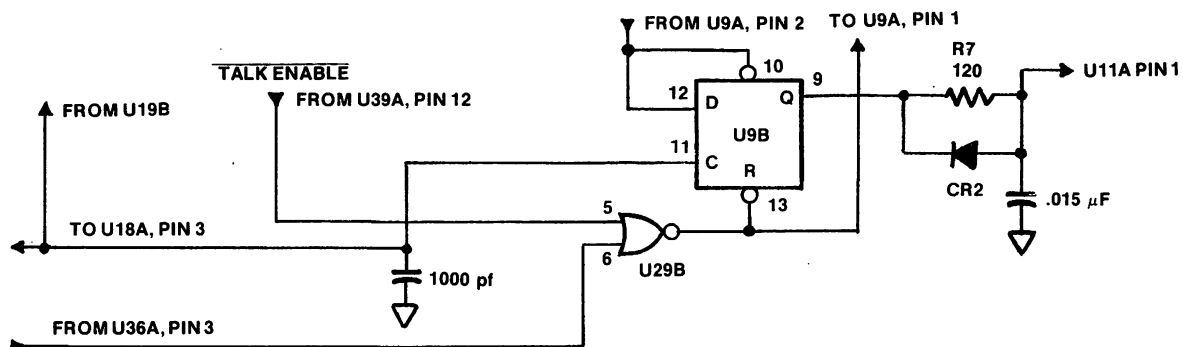


Supersedes:
None**HP MODEL 5345A ELECTRONIC COUNTER****All Serial Prefixes****FAILURE TO RESPOND PROPERLY TO A SERIAL POLL
BY A SERIES 200 CONTROLLER**

A modification is necessary in order to allow the HP 5345A Option 012 counter to respond properly to a serial poll by a Series 200 controller. When polled by a Series 200 controller, the HP 5345A Option 012 will intermittently hang up the HP-IB and cause the handshake operation to cease. This is due to the controller bringing ATN false (+5 volts, negative true logic) before the HP 5345A can internally process the RFD false-to-true transition between the Serial Poll Enable (SPE) byte and the status byte transmission. The ATN line must be false to signify the presence of a data byte rather than a command byte on the HP-IB.

A race condition is set up at the clock (pin 11) and reset (pin 13) inputs to U9B on the A12 board. Clocking must occur after the reset signal for the proper logic levels to drive U11A which eventually drives the DAV line via U9A. Both the clock and reset inputs to U9B are derived from the ATN signal. A 1000 pf capacitor, HP Part Number 0160-4040 should be connected from pin 11 of U9B to pin 7 of U9B in order to delay the clock until after the reset has occurred.

This is how the modification appears on the schematic of the A12 board:



Record the changes in your operating and service manual.

D/OF/WA

3/84-02/BG



For more information contact a local Hewlett-Packard Office. (Hewlett-Packard has 200 Sales and Service Offices in 75 countries).
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