

S E R V I C E N O T E

SUPERSEDES: None

4142B Modular DC Source/Monitor

Serial Numbers: 2946J00000 / 3121J03351

ROM replacement to correct PT command default pulse width

To Be Performed By: Agilent-Qualified Personnel

Parts Required: See text.

Situation:

The firmware revision is 3.0, 3.1, 4.0, 4.1 or 4.2.

The problem shows the following phenomenon:

When pulse_period is not specified with the PT command, the program execution speed becomes slower (500 ms per PT command). By specifying zero as pulse_period, the phenomenon disappears.

Continued

DATE: June 1996

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:					
MODIFICATION RECOMMENDED					
ACTION CATEGORY:	<input type="checkbox"/> IMMEDIATELY <input checked="" type="checkbox"/> ON SPECIFIED FAILURE <input type="checkbox"/> AGREEABLE TIME	STANDARDS:	Labor 1.0 Hour		
LOCATION CATEGORY:	<input type="checkbox"/> CUSTOMER INSTALLABLE <input type="checkbox"/> ON-SITE <input checked="" type="checkbox"/> SERVICE CENTER	SERVICE INVENTORY:	<input type="checkbox"/> RETURN <input type="checkbox"/> SCRAP <input checked="" type="checkbox"/> SEE TEXT	USED PARTS:	<input type="checkbox"/> RETURN <input checked="" type="checkbox"/> SCRAP <input type="checkbox"/> SEE TEXT
AVAILABILITY:	PRODUCT'S SUPPORT LIFE		AGILENT RESPONSIBLE UNTIL: June 1998		
AUTHOR: MO	ENTITY: 3300	ADDITIONAL INFORMATION:			

Solution/Action:

If the above phenomena is verified, update the 4142B’s firmware revision to 4.3 by replacing ROMs.

The following table indicates the required ROM part number(s) for each firmware revision:

Revision	ROM Part Number	Description
3.0/3.1	04142-86845	ROM Set, Rev 4.3
4.0	04142-85038	ROM, U83
	04142-85039	ROM, U85
	04142-85124	ROM, U51
	04142-85125	ROM, U67
	04142-85126	ROM, U52
	04142-85127	ROM, U68
	04142-85144	ROM, U48
	04142-85145	ROM, U64
4.1/4.2	04142-85124	ROM, U51
	04142-85125	ROM, U67
	04142-85126	ROM, U52
	04142-85127	ROM, U68
	04142-85144	ROM, U48
	04142-85145	ROM, U64

To replace ROMs, use the following procedure:

1. Turn the 4142B off and wait at least ten seconds.
2. Remove the CPU module from the 4142B mainframe.
3. Remove the side panel of the CPU module.
4. Remove the ROMs per the above table. For example, if the current revision is 4.1, remove U48, U51, U52, U64, U67 and U68. If the current revision is 3.0 or 3.1, remove all 16 ROMs. See Figure 1 for the ROM locations.
5. Place the new ROMs.
6. Reinstall the side panel of the CPU module, and reinstall the CPU module in the 4142B.
7. Turn the 4142B on. Confirm that no error appears in the ERROR/FAILURE display of the 4142B front panel.

		04142-85126 U52	04142-85127 U68
		04142-85124 U51	04142-85125 U67
		04142-85022 U50	04142-85023 U66
04142-85030 U84	04142-85031 U86	04142-85020 U49	04142-85021 U65
04142-85038 U83	04142-85039 U85	04142-85144 U48	04142-85145 U64
		04142-85032 U47	04142-85033 U63
Front Panel Side			

Figure 1. ROM Locations